

FIG. 1

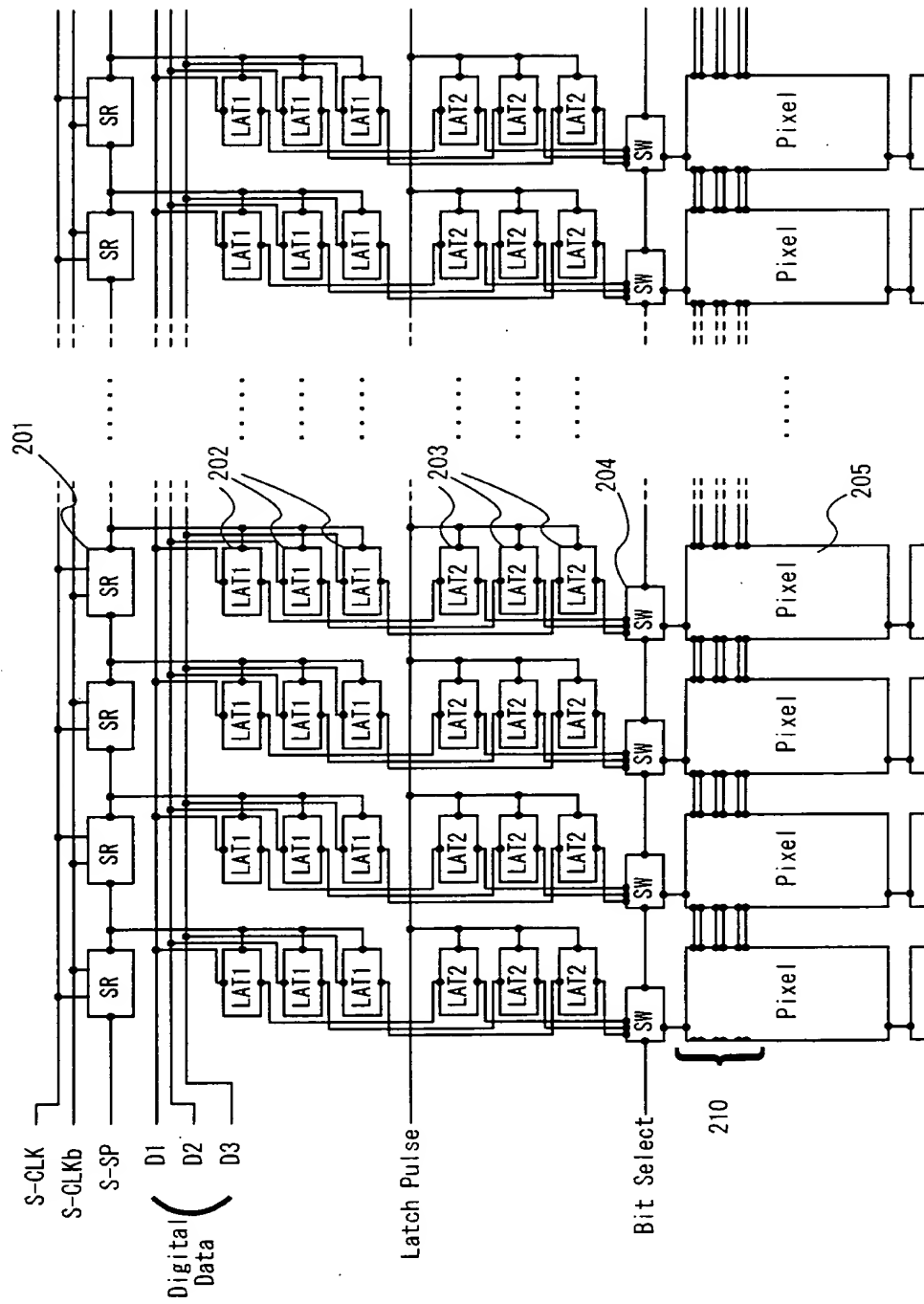
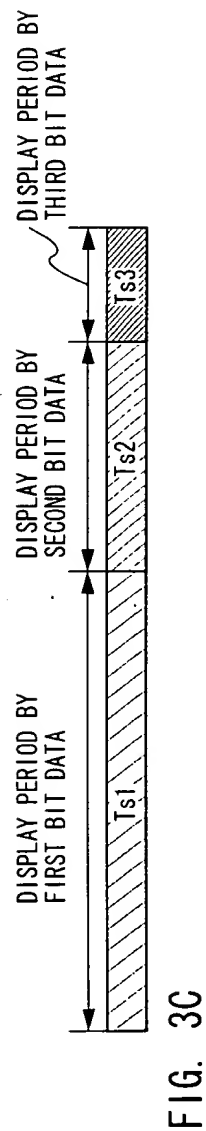
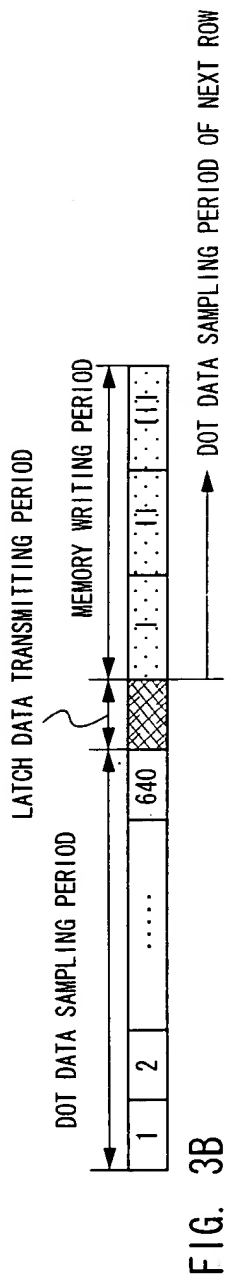
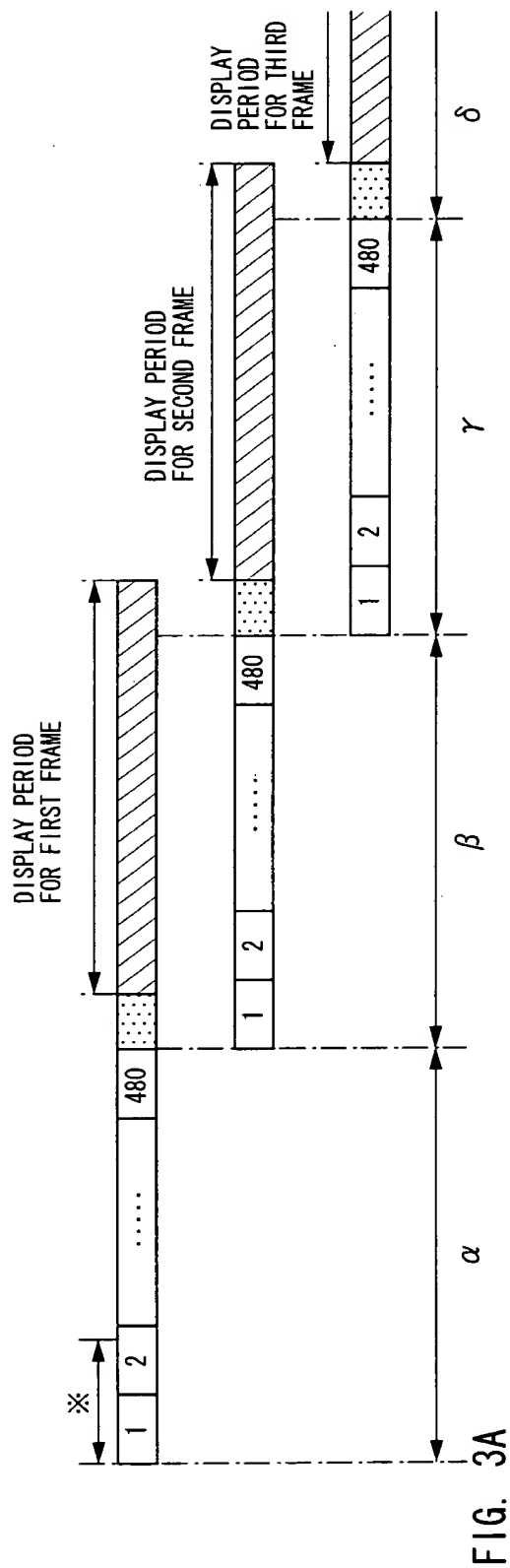


FIG. 2



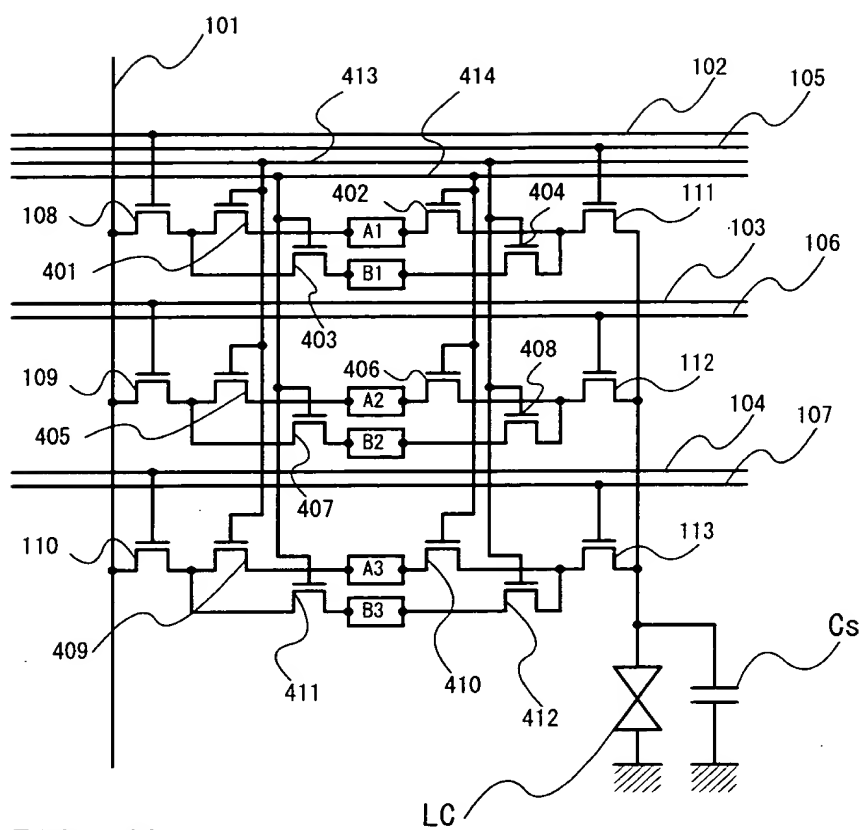


FIG. 4A

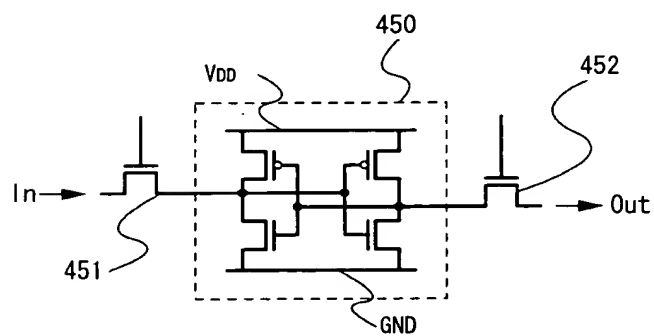


FIG. 4B

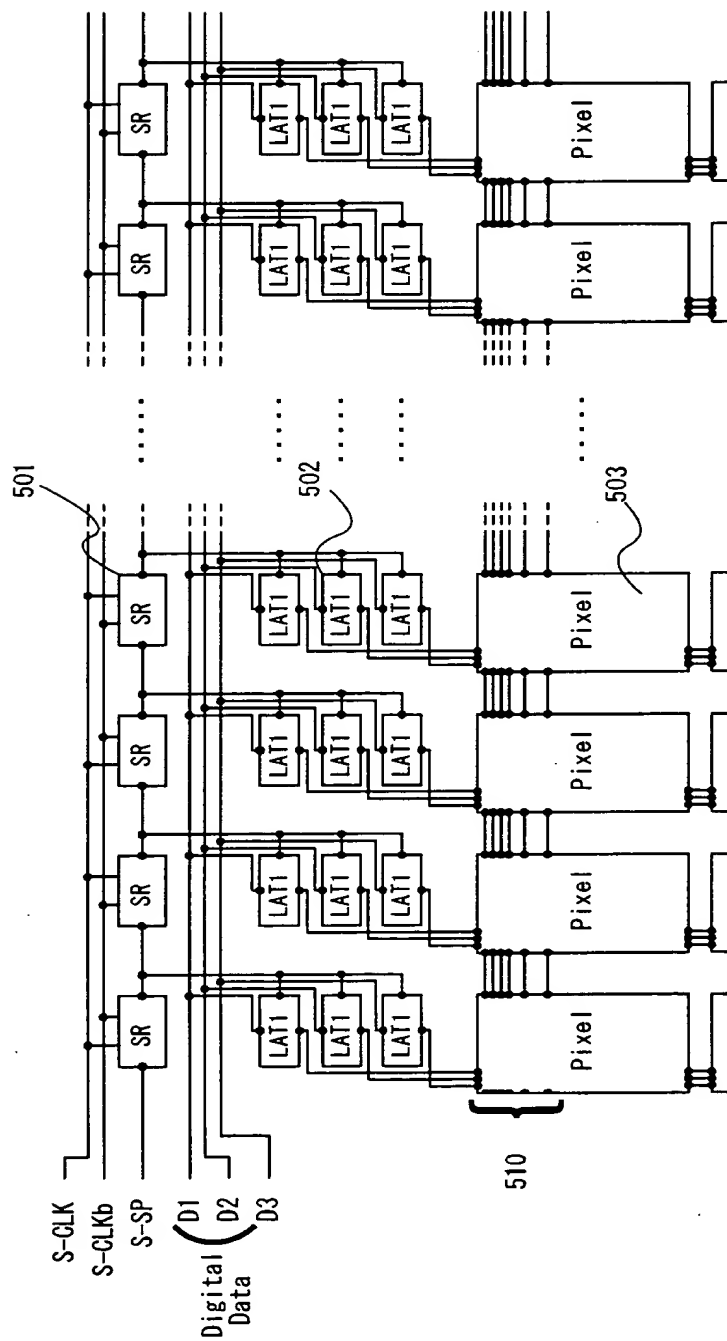


FIG. 5

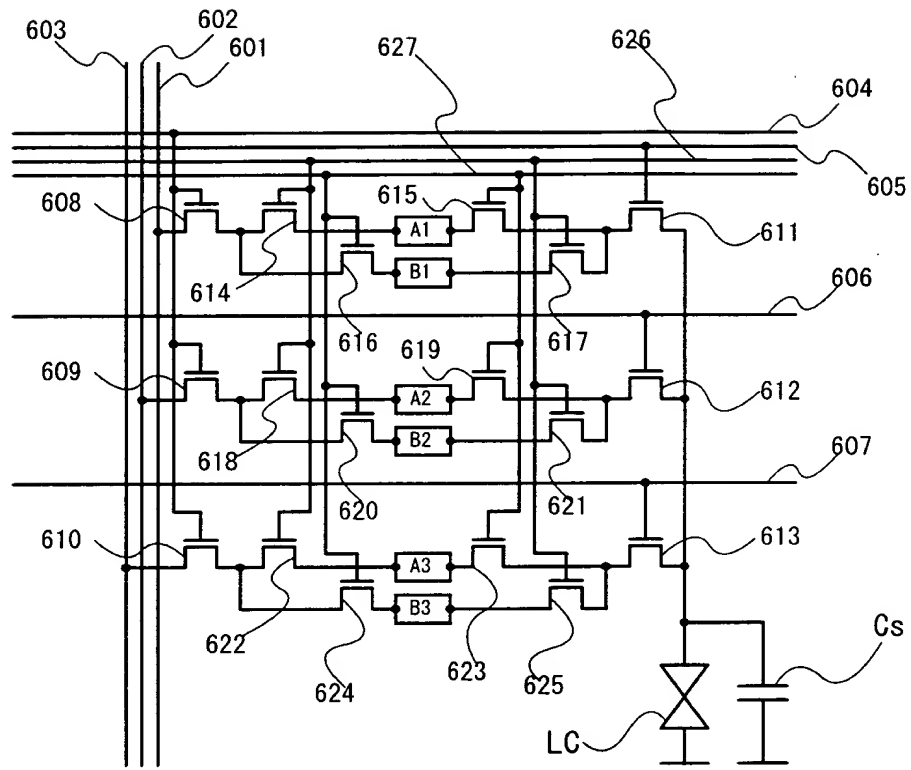
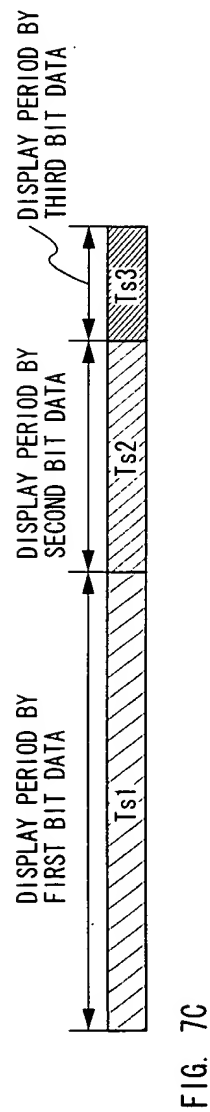
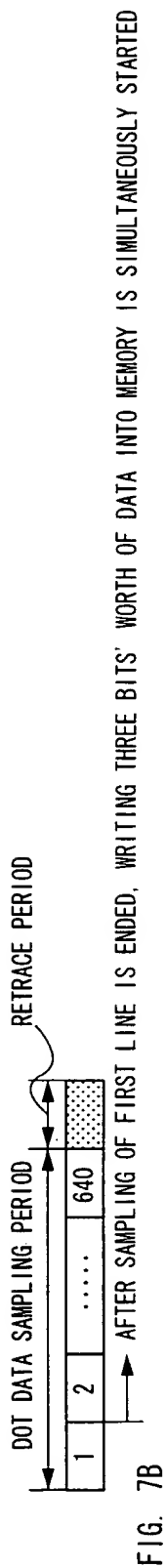
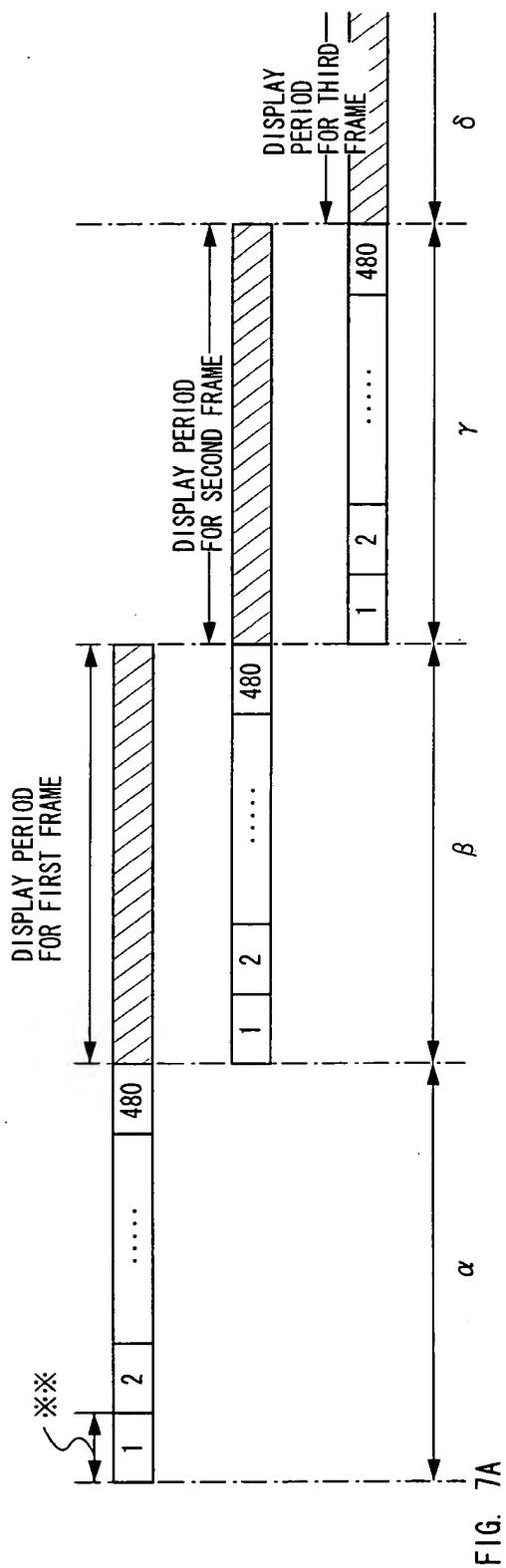


FIG. 6



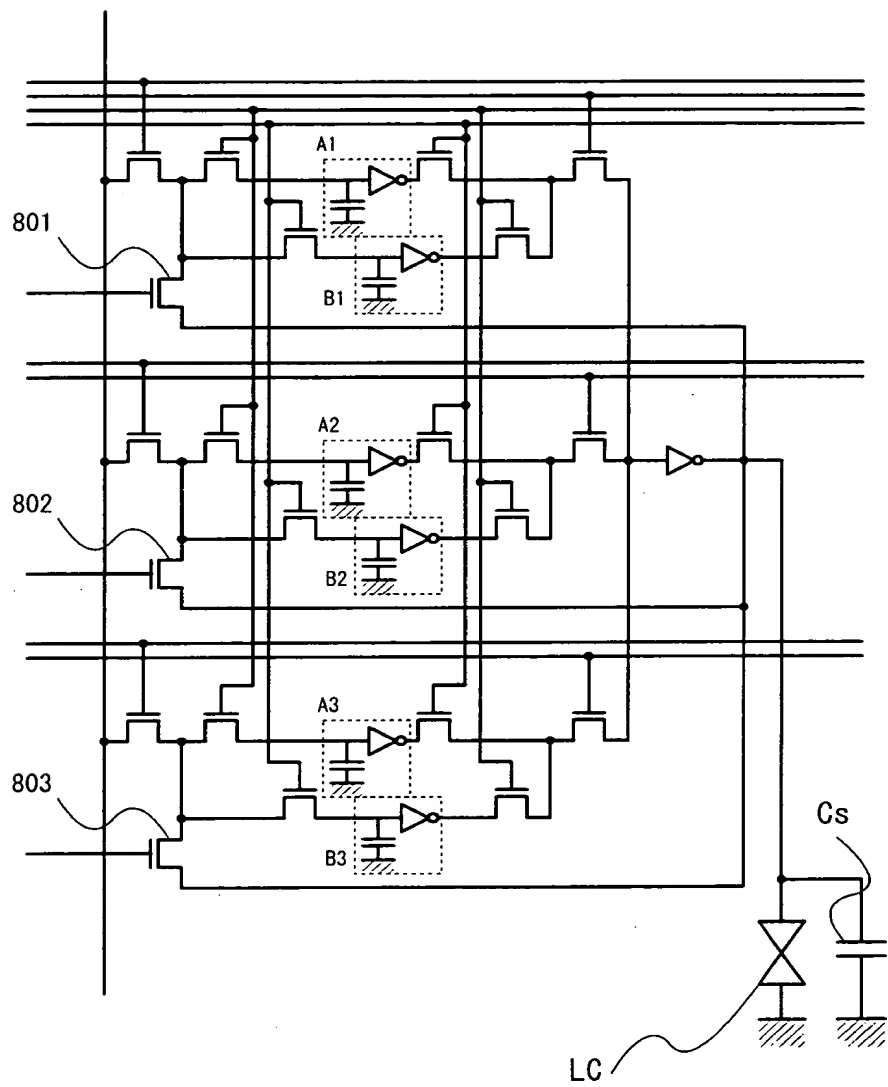


FIG. 8

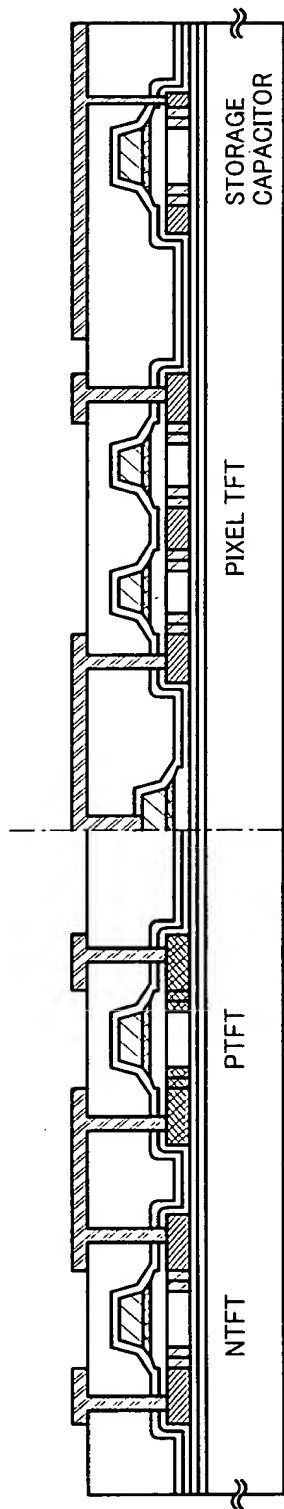


FIG. 9A

DRIVING CIRCUIT
PORTION

PIXEL PORTION

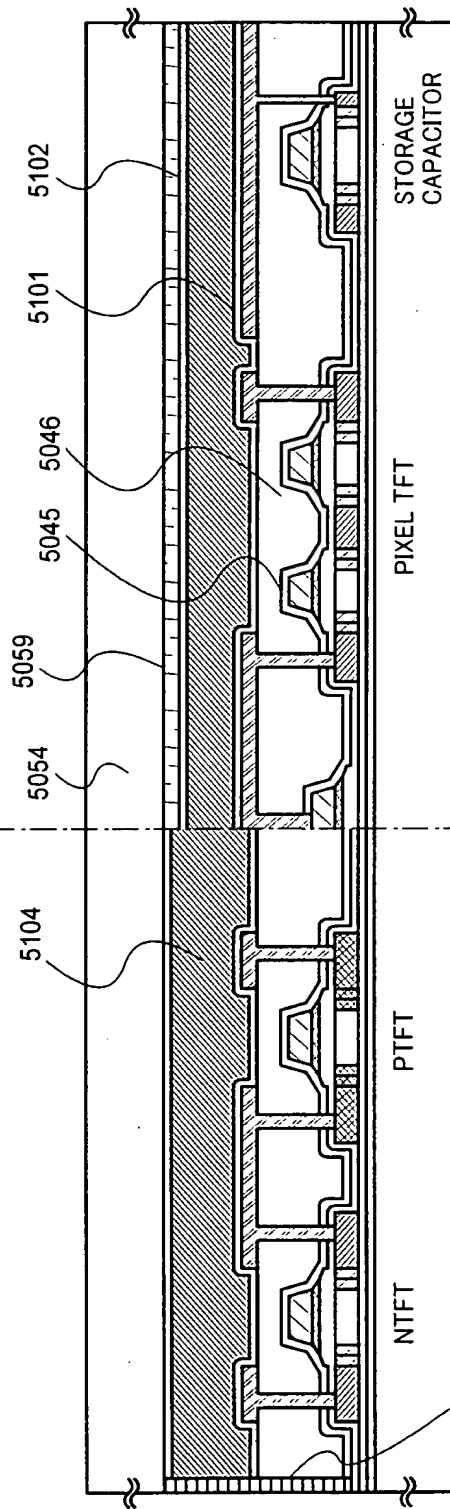


FIG. 9B

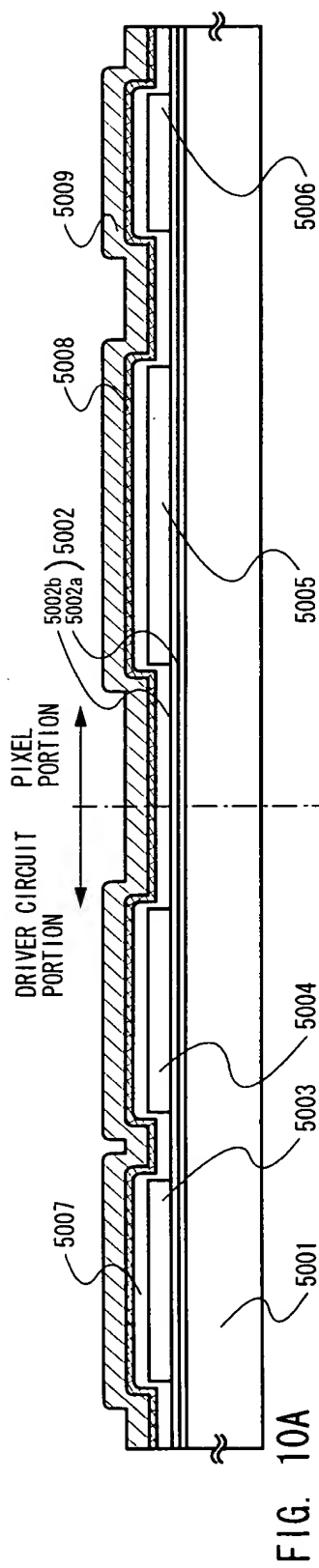


FIG. 10A

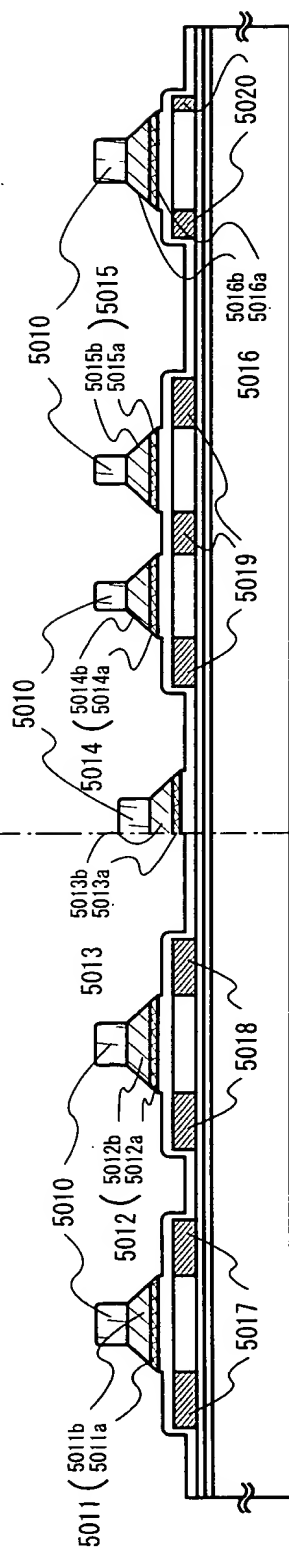


FIG. 10B

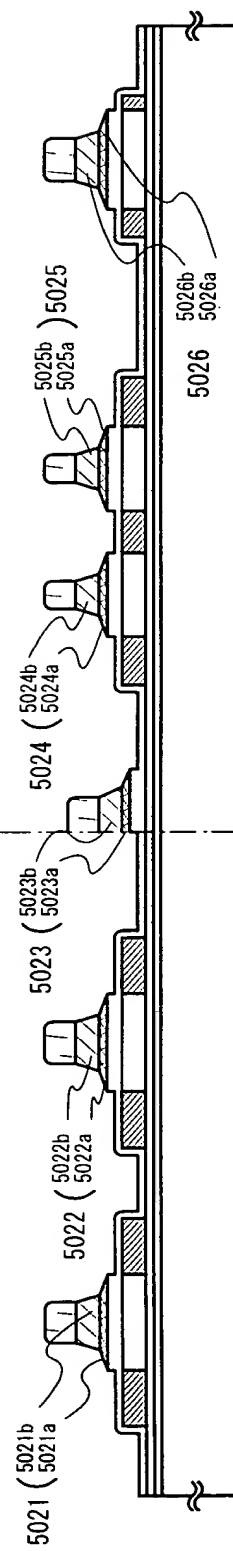


FIG. 10C

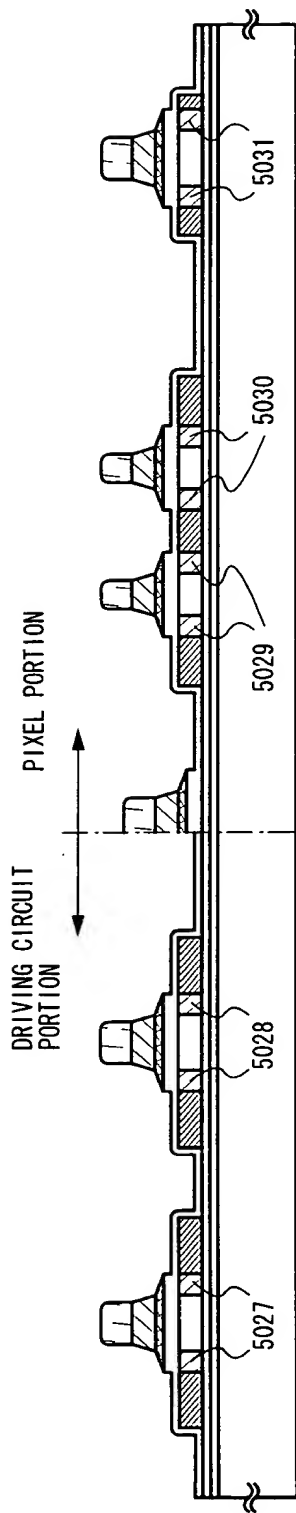


FIG. 11A

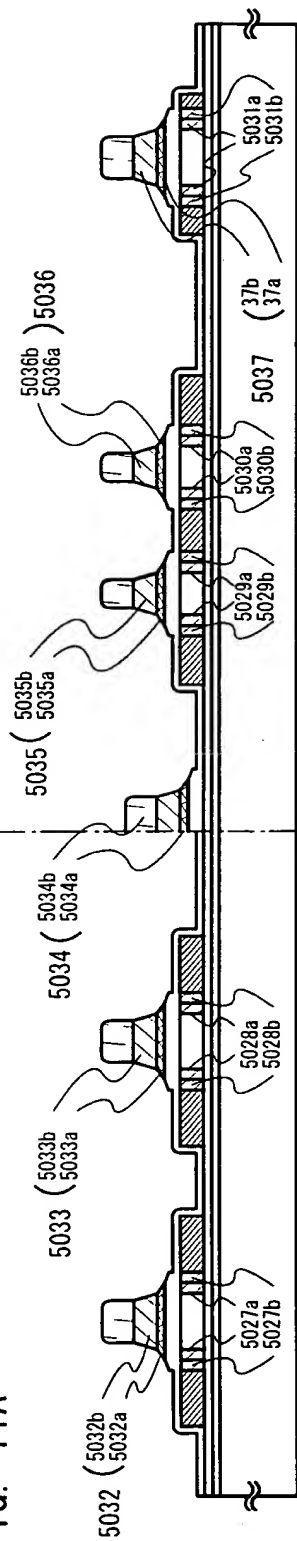


FIG. 11B

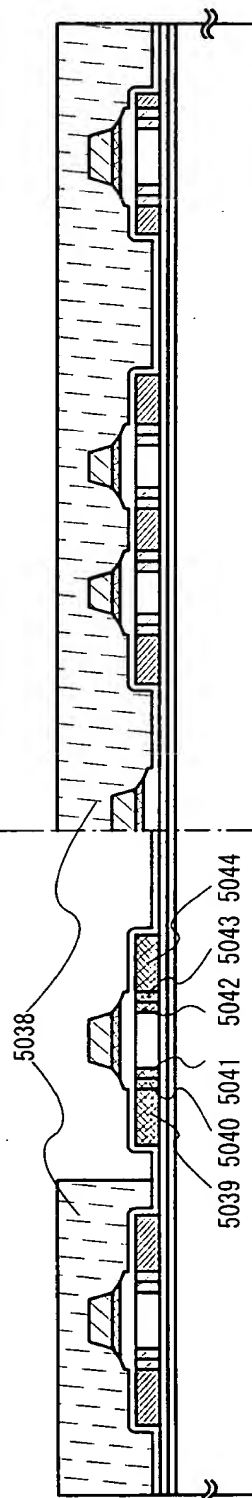


FIG. 11C

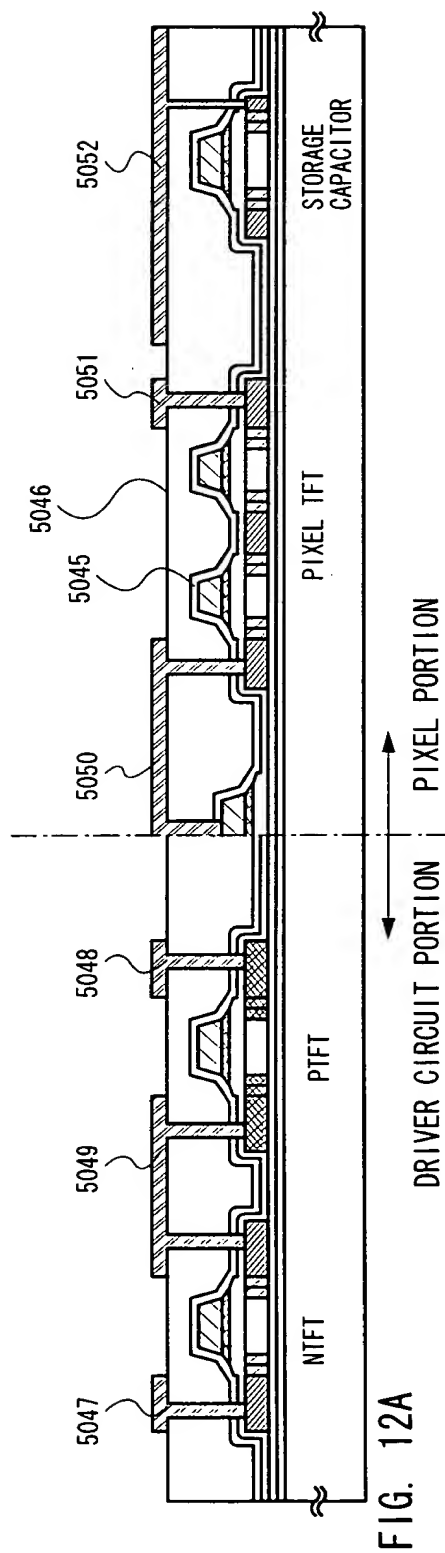


FIG. 12A

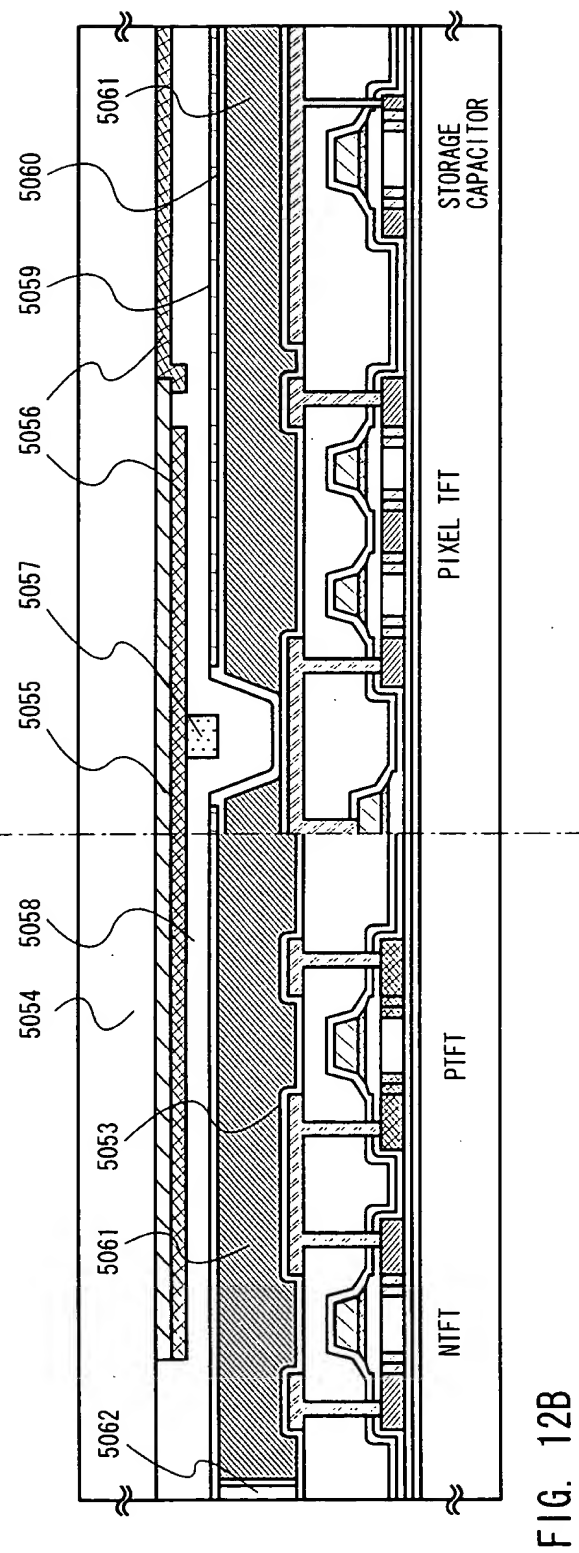


FIG. 12B

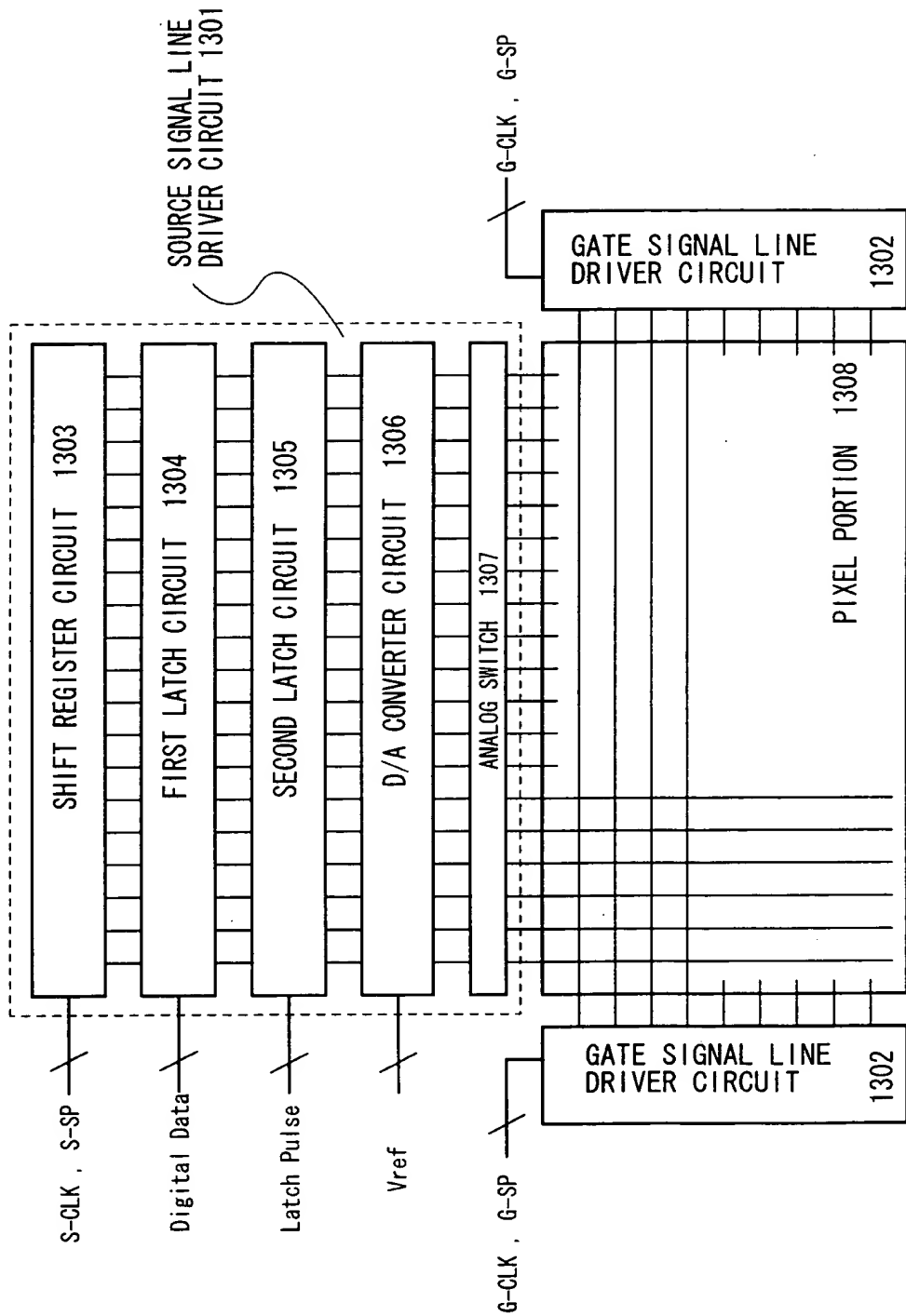


FIG. 13

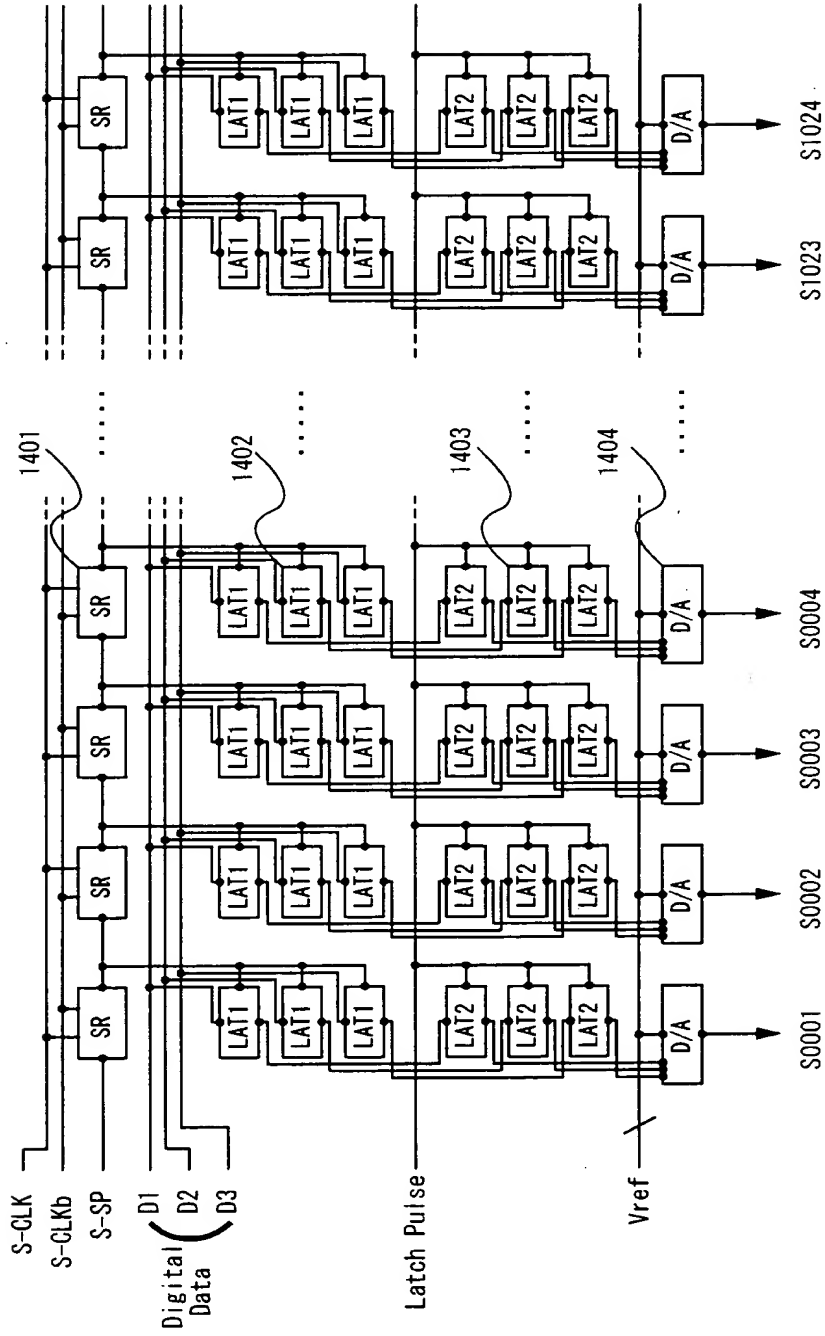
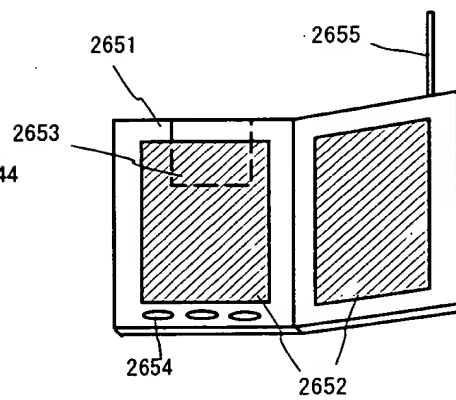
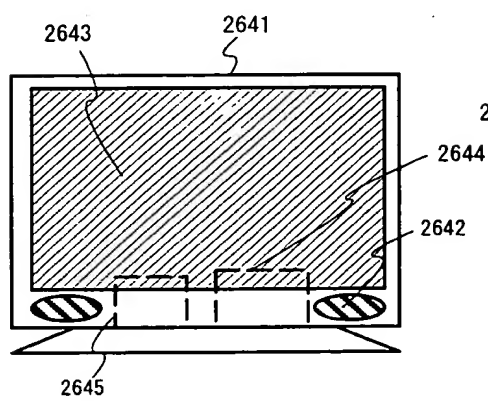
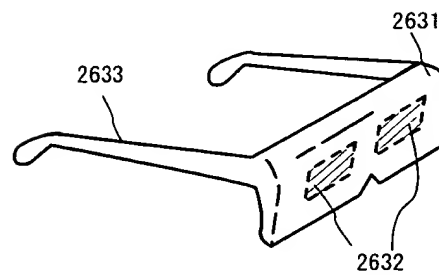
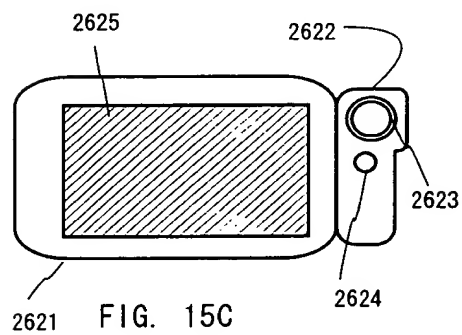
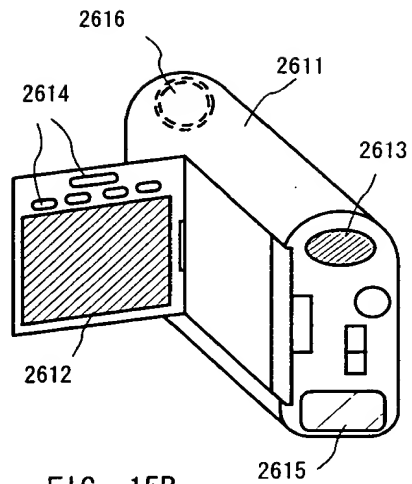
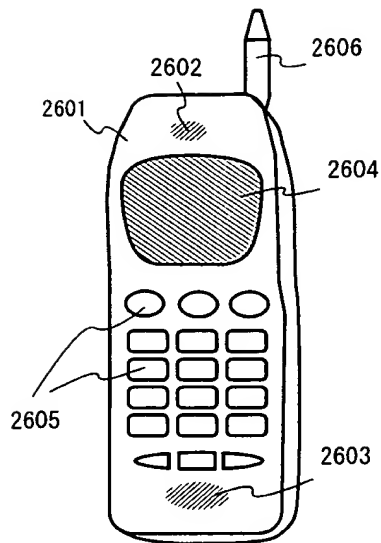


FIG. 14



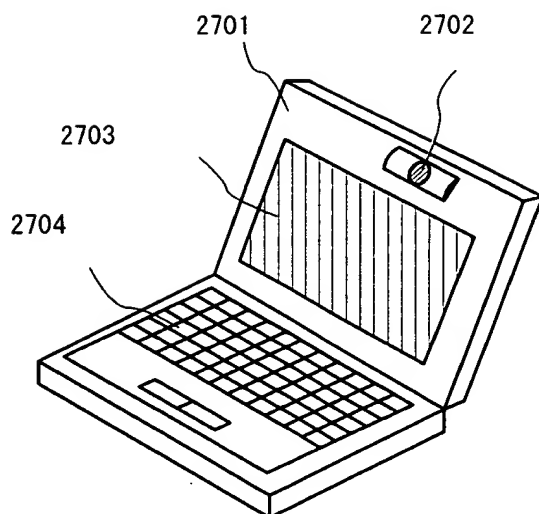


FIG. 16A

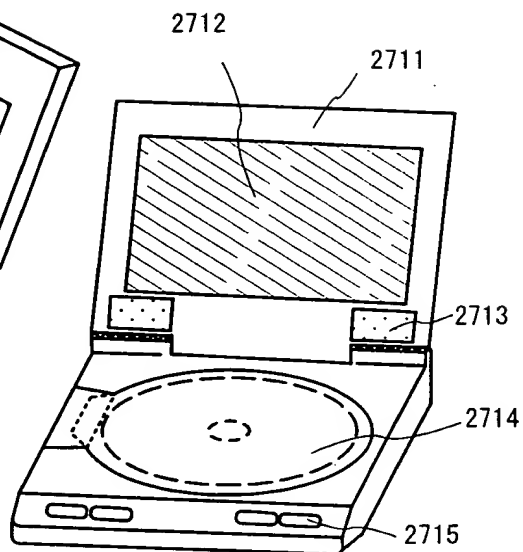


FIG. 16B

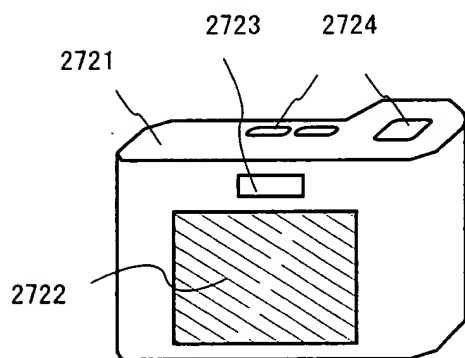


FIG. 16C

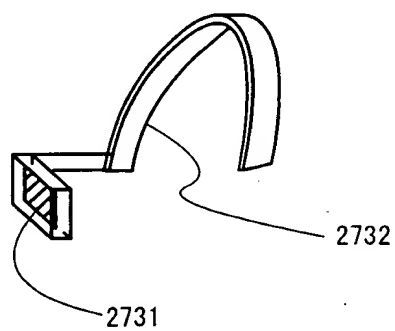


FIG. 16D

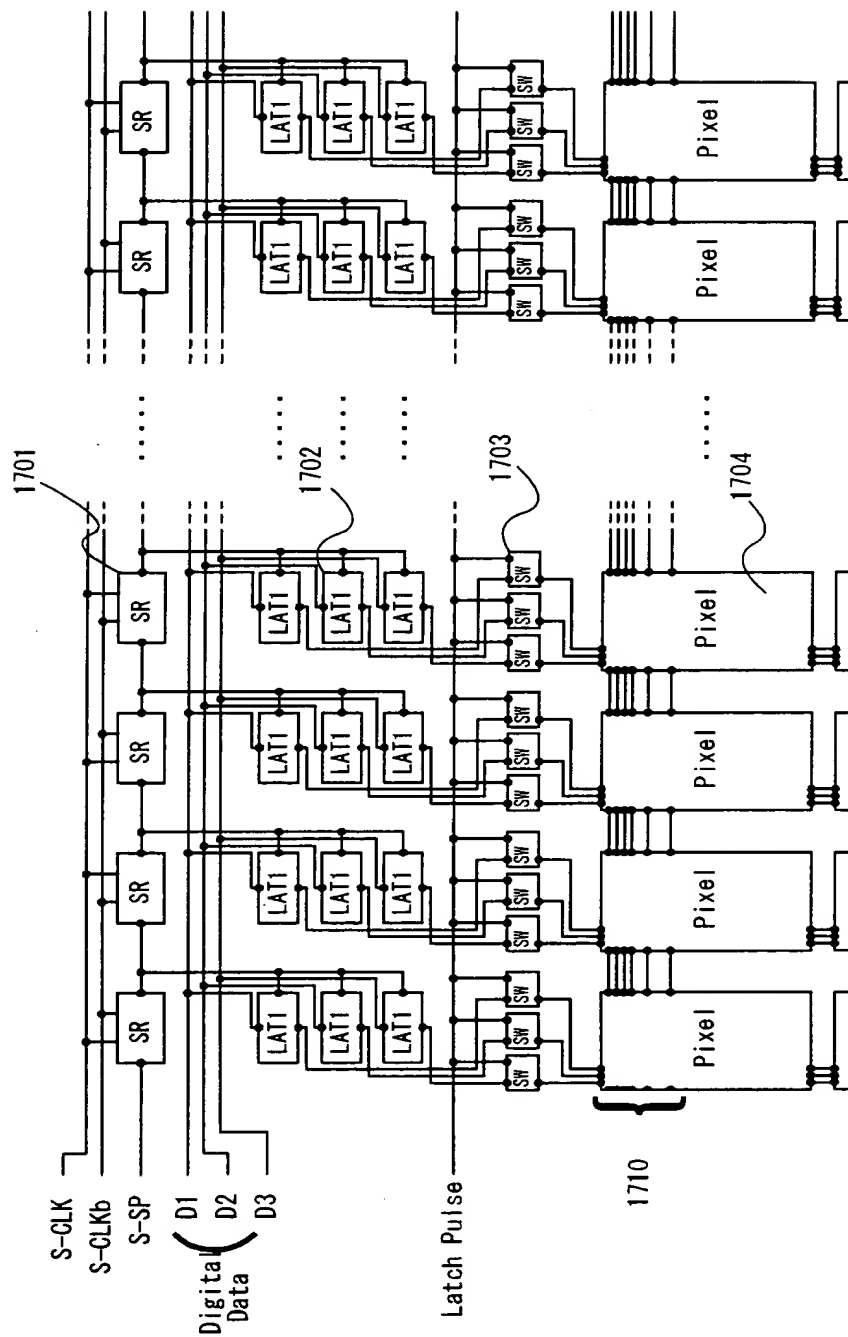


FIG. 17

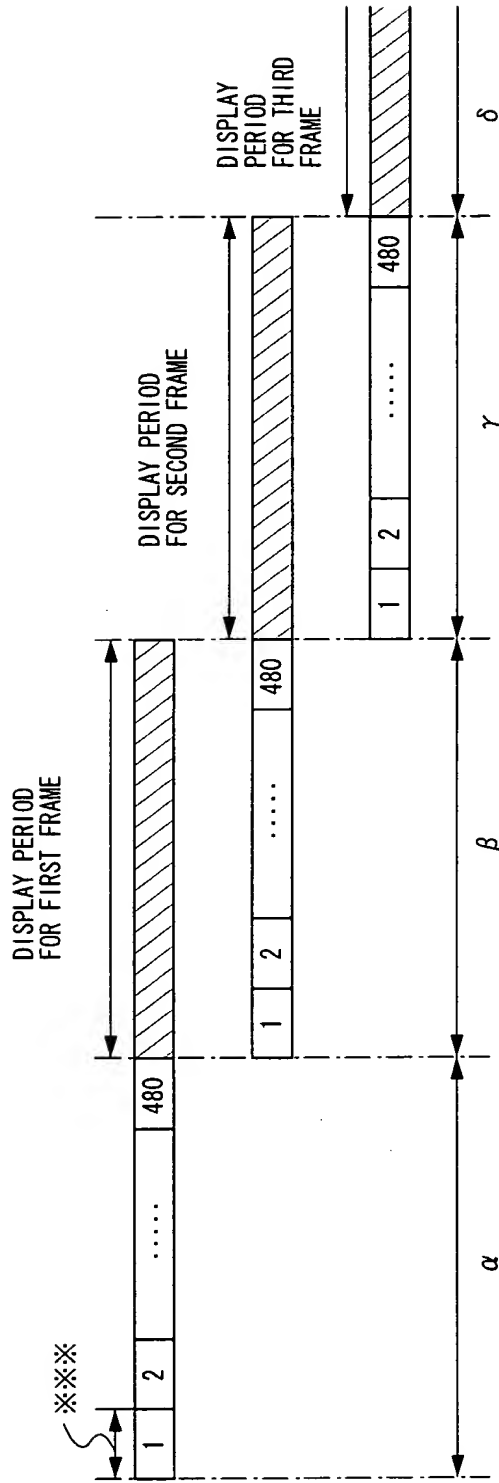


FIG. 18A

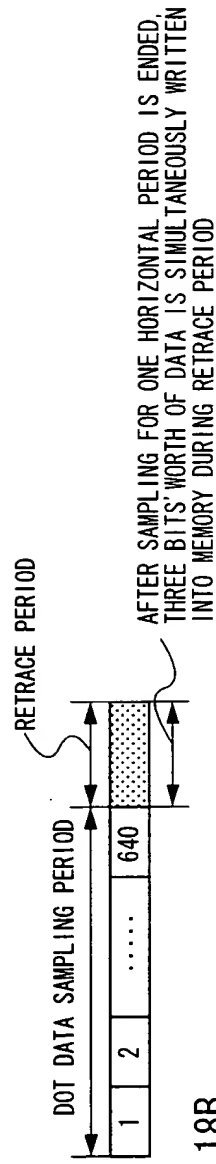


FIG. 18B

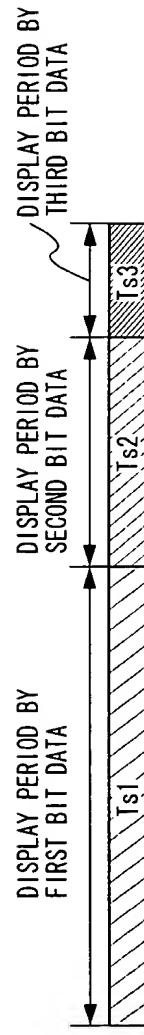


FIG. 18C

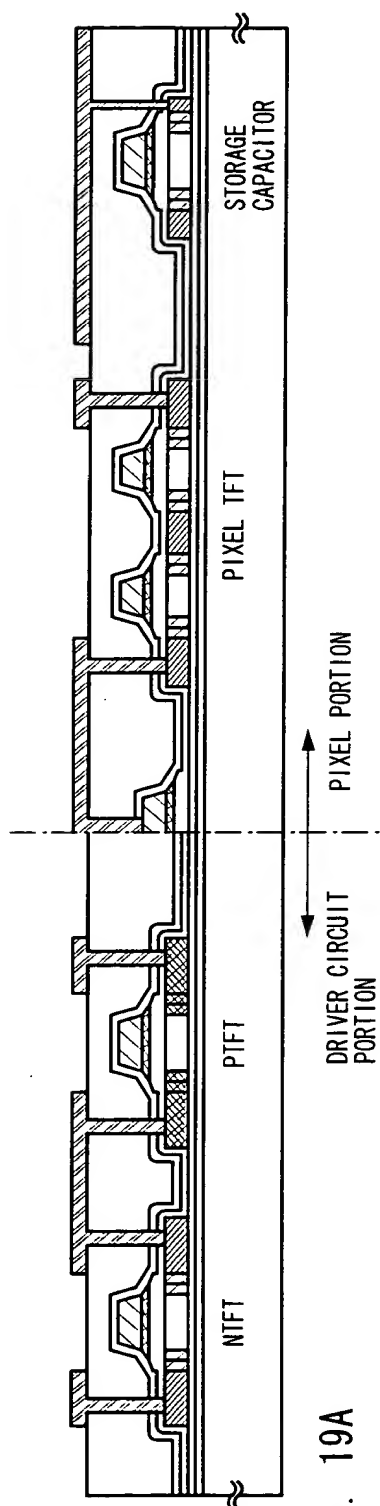


FIG. 19A

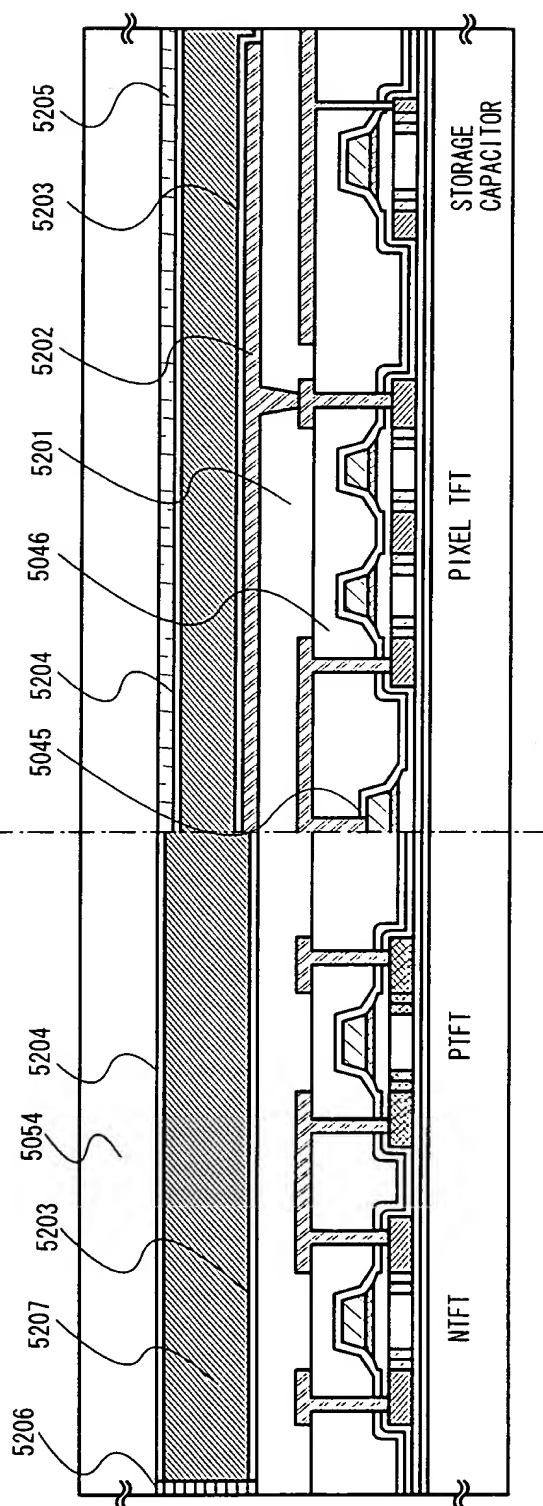


FIG. 19B

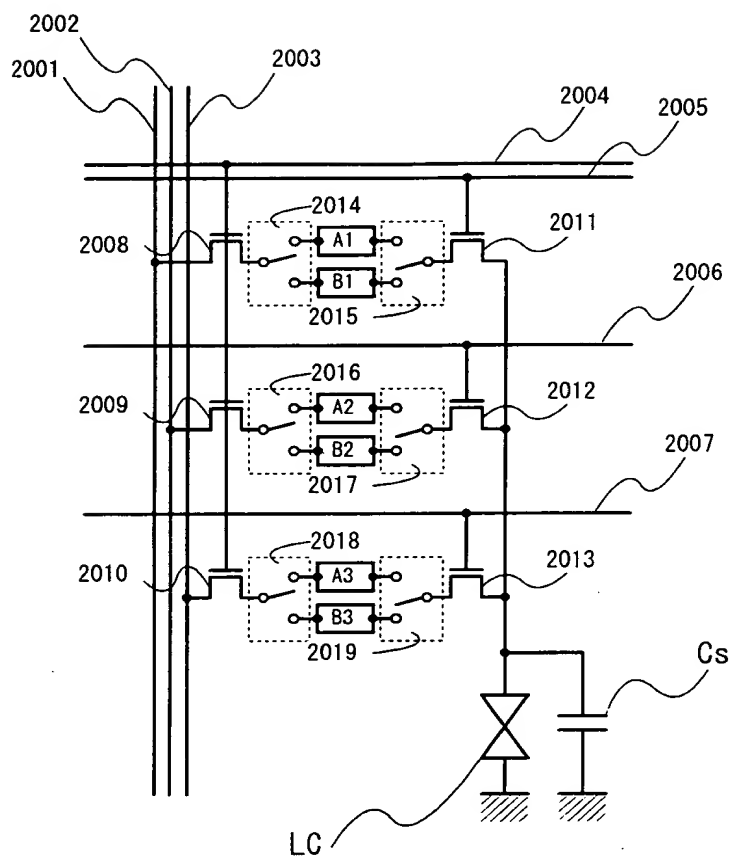


FIG. 20